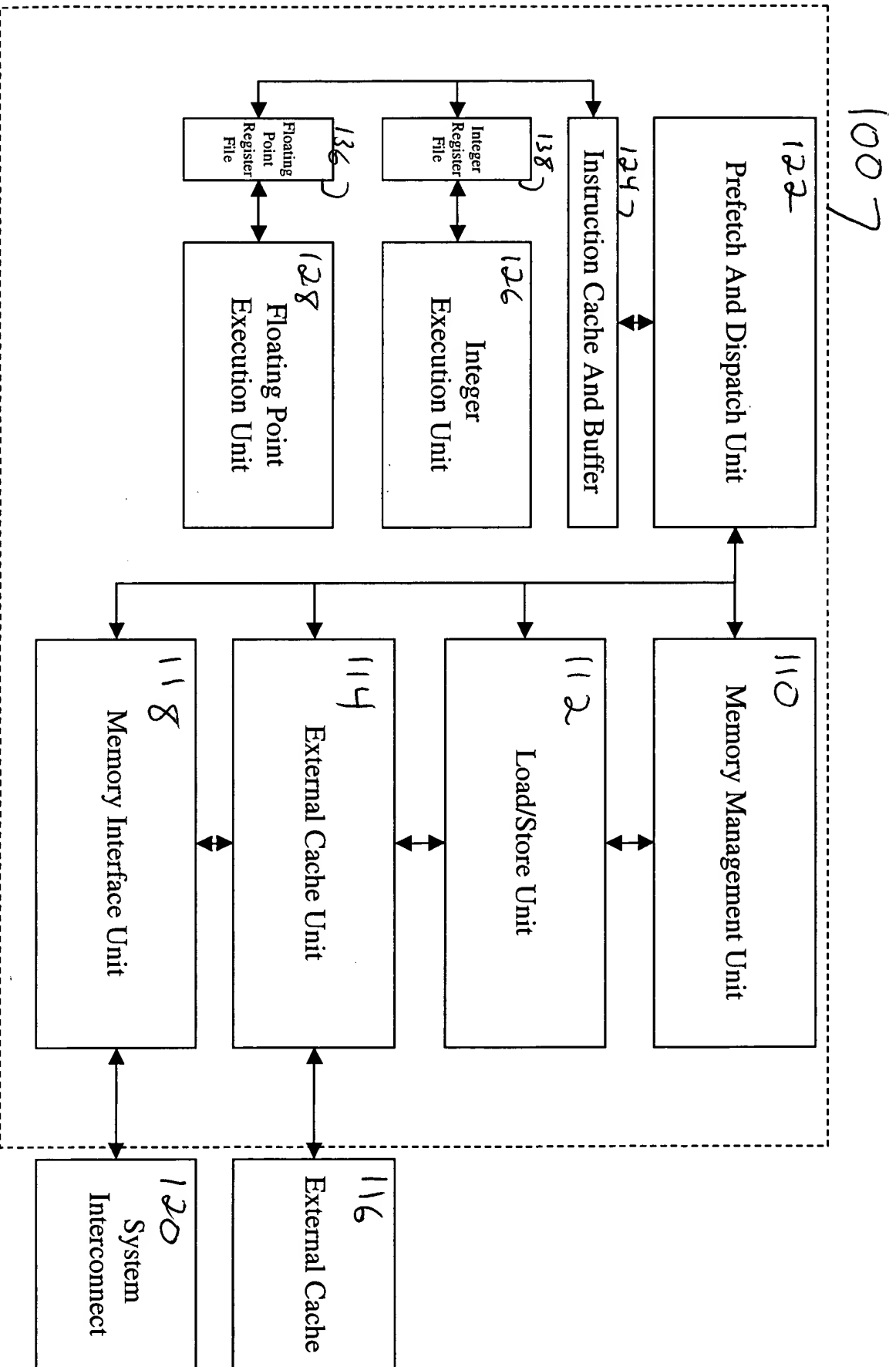


Figure 1
Prior Art Processor



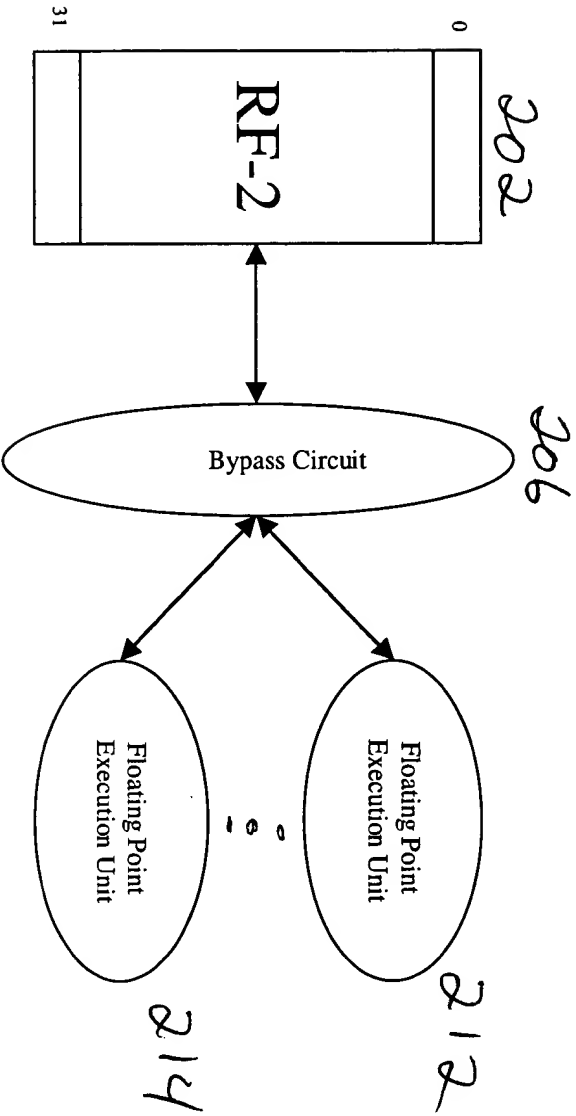
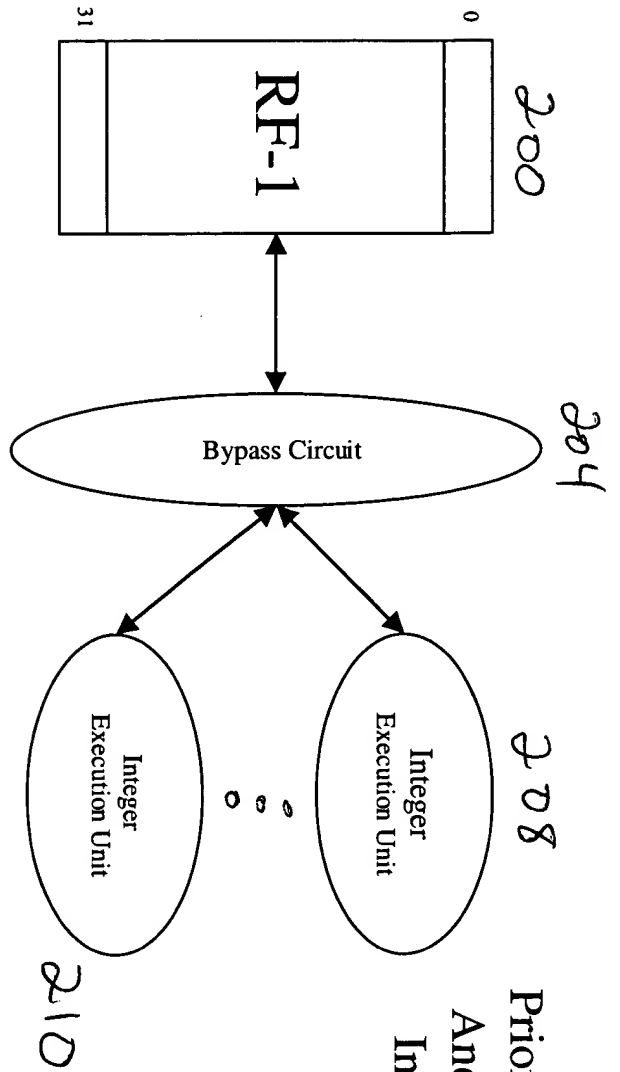
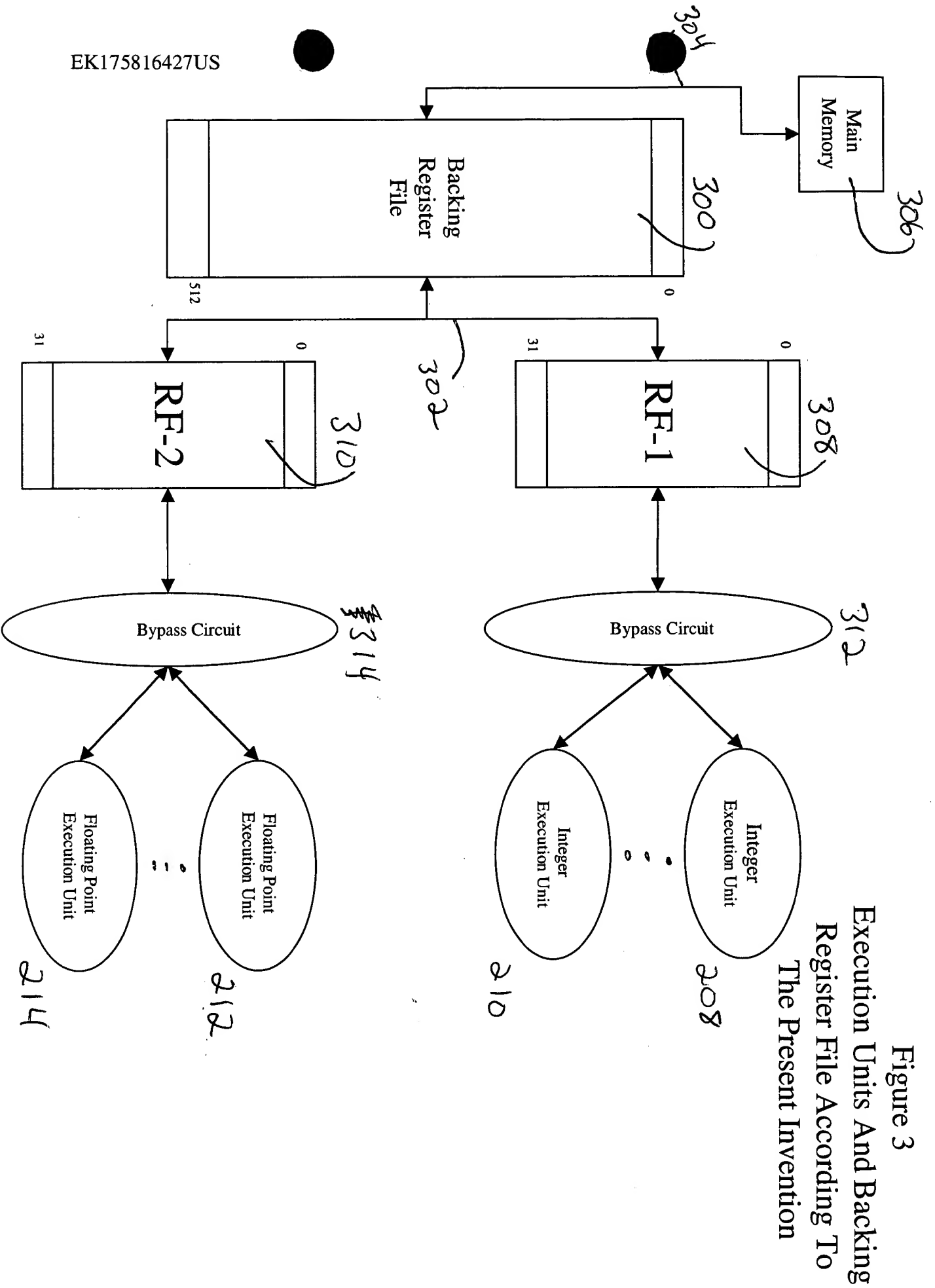


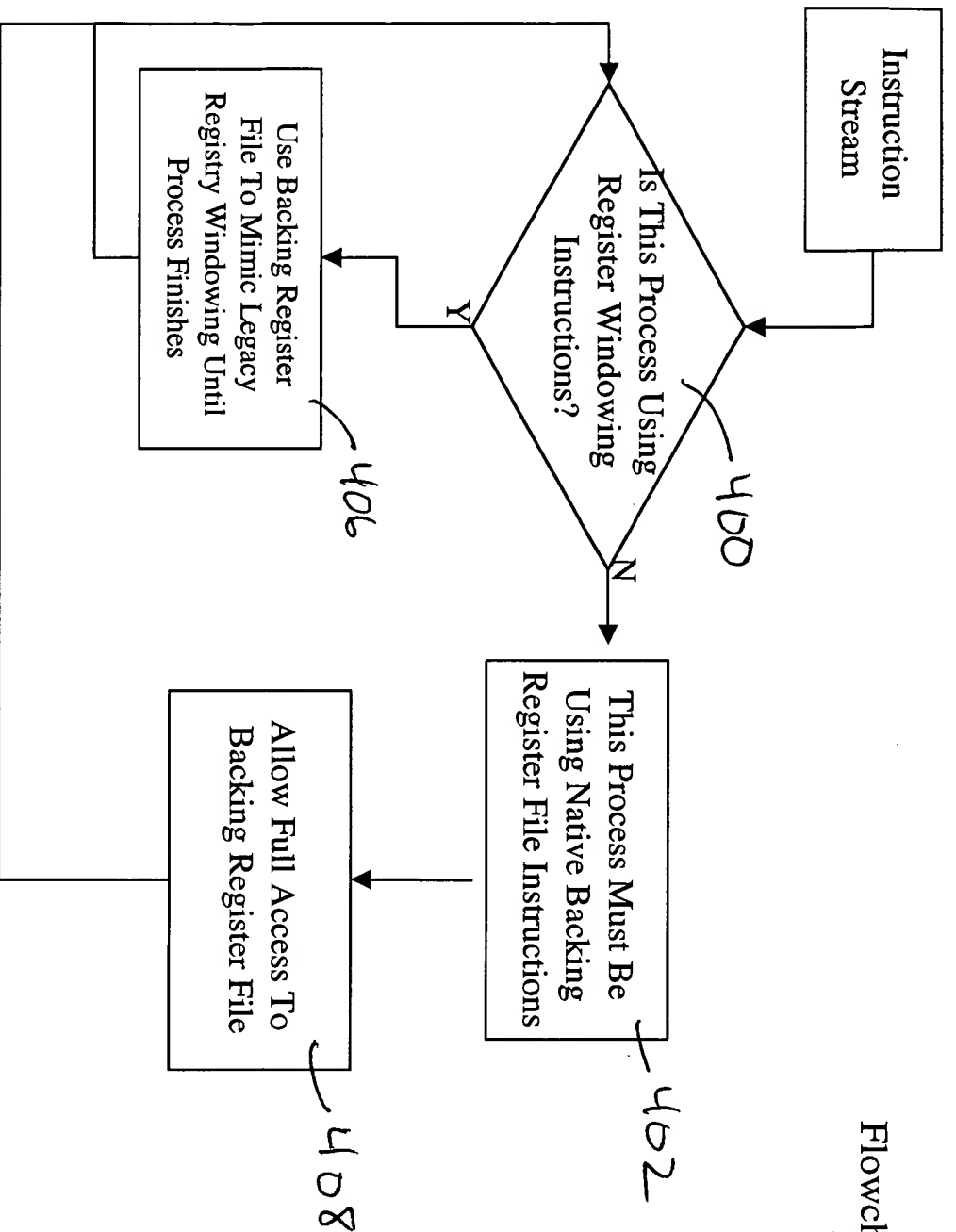
Figure 2
Prior Art Execution Units
And Register Files With
Increased Parallelism
Using Multiple
Execution Units

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Figure 4
Flowchart Example Of Initializing
The Present Invention



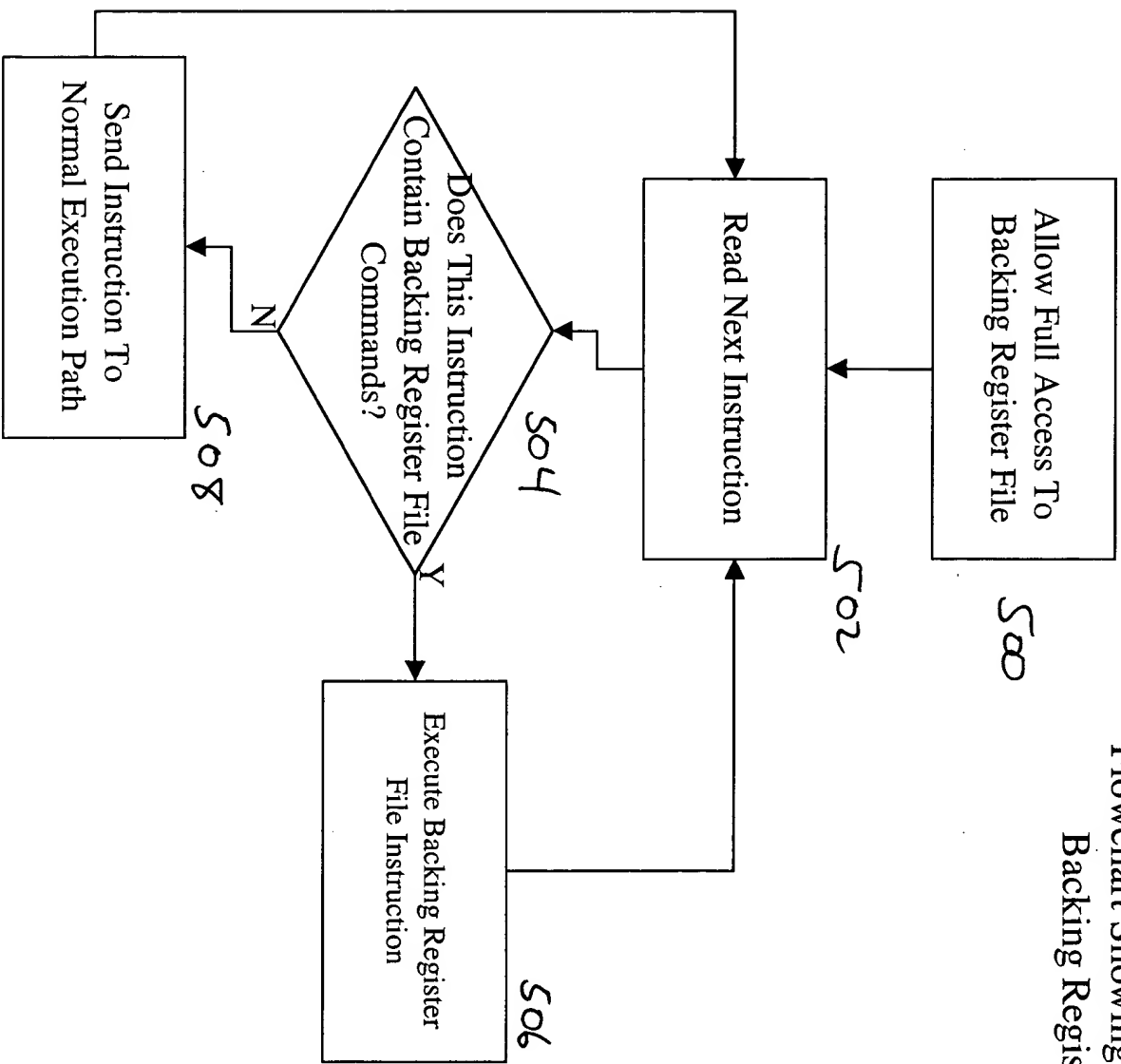


Figure 5
Flowchart Showing Use Of The
Backing Register File

Figure 6a
Data Organization: Singly Linked List

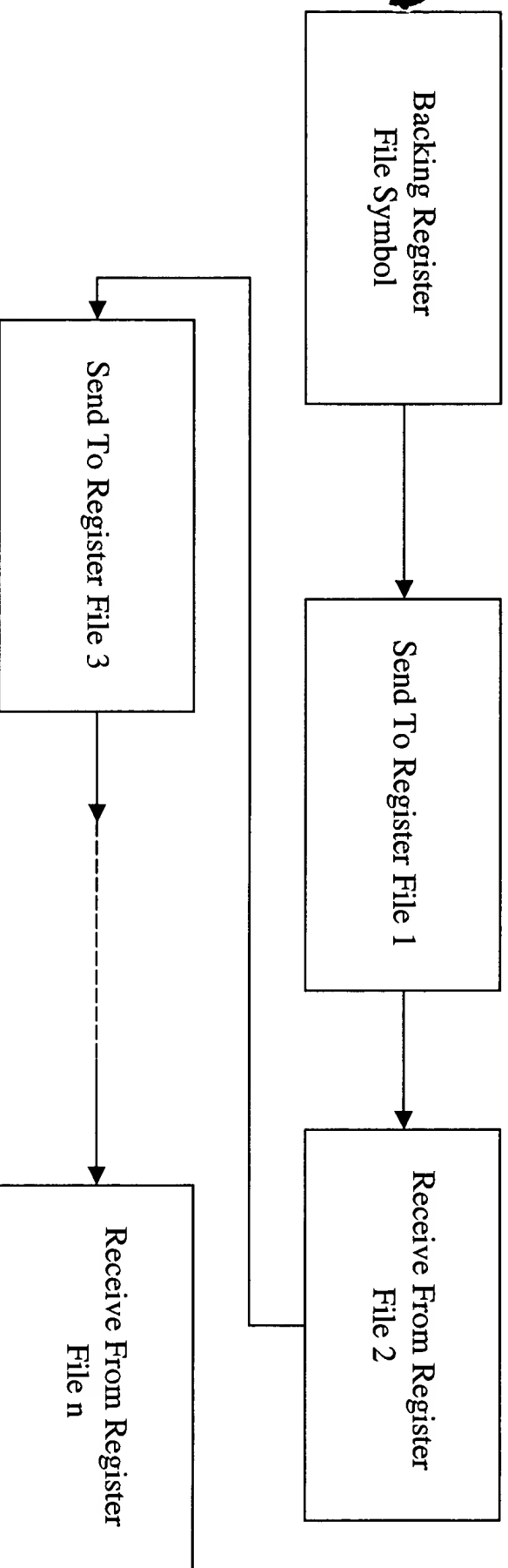


Figure 6b
Data Organization: Byte Stream

